

# Application Note

## From Astec Power

Rev. 00

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### APPLICATION OVERVIEW: Hold-up Time Versus Output Power

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### PRODUCT: LPS40 and LPT40 Series

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### ORIGINATOR: Tom Tillman, Product Line Manager

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### DESCRIPTION

The LPS40 and LPT40 range of power supplies have been designed to provide a minimum of 20mS of hold-up time at 115Vac supply. This allows the power supply to continue to provide a regulated DC output in the event of short interruptions of the AC supply. However, the actual hold-up time is dependent not only on the AC supply, but also the output power level. At higher output power levels, energy is drawn from the power supply's internal capacitance at a faster rate than at lower power levels, resulting in a shorter hold-up time.

The below graph shows the relationship between the typical hold-up time available with respect to the output power level for both 115Vac and 230Vac AC line voltages.

