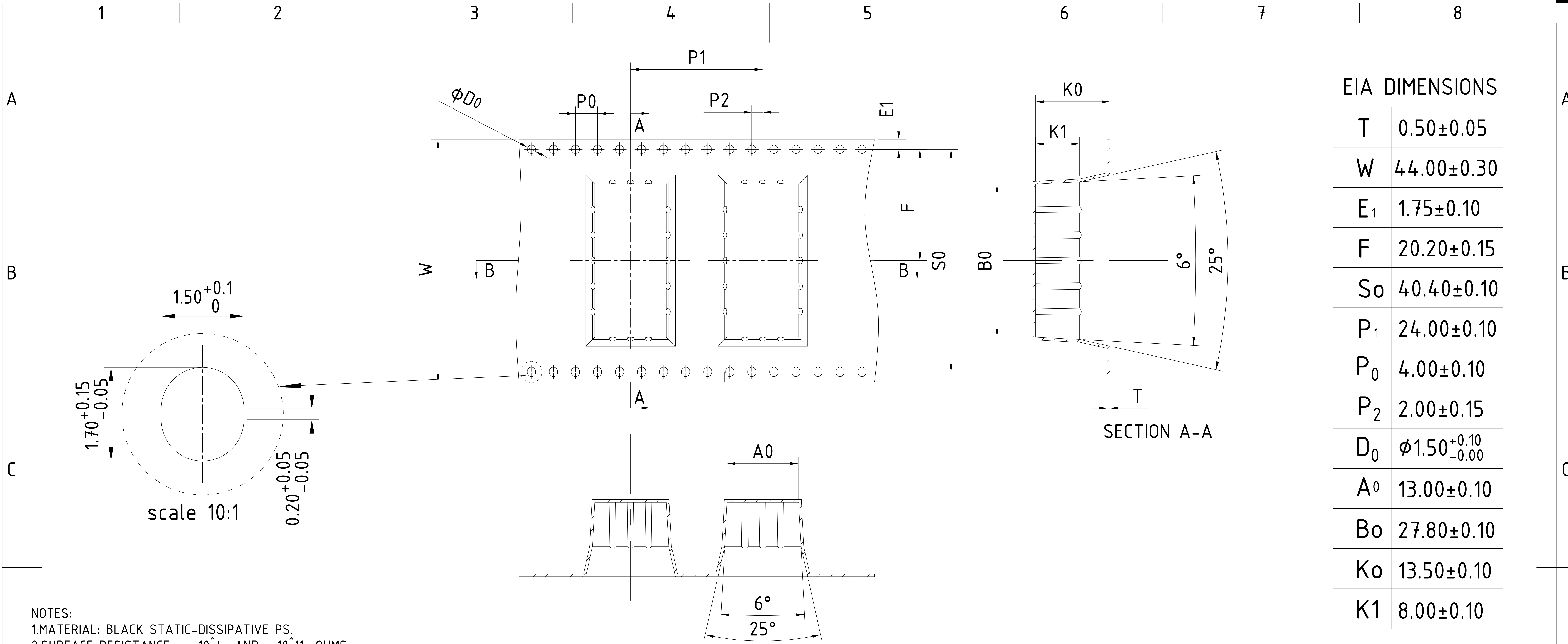


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EIA DIMENSIONS	
T	0.50±0.05
W	44.00±0.30
E ₁	1.75±0.10
F	20.20±0.15
S ₀	40.40±0.10
P ₁	24.00±0.10
P ₀	4.00±0.10
P ₂	2.00±0.15
D ₀	$\phi 1.50^{+0.10}_{-0.00}$
A ₀	13.00±0.10
B ₀	27.80±0.10
K ₀	13.50±0.10
K ₁	8.00±0.10

- NOTES:
- 1.MATERIAL: BLACK STATIC-DISSIPATIVE PS.
 - 2.SURFACE RESISTANCE : $\geq 10^4$ AND $< 10^{11}$ OHMS.
 - 3.THE ELECTROSTATIC POTENTIALS OF THE PART SURFACE SHOULD BE LESS THAN 100V.
 - 4.10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE +/-0.20MM.
 - 5.CARRIER CAMBER NOT TO EXCEED 1MM IN 100MM.
 - 6.A0 AND B0 MEASURED ON A PLANE 0.3MM ABOVE THE BOTTOM OF THE POCKET.
 - 7.K0 MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER.
 - 8.ALL DIMENSIONS MEET EIA-481-D REQUIREMENTS.
 - 9.UNLESS OTHERWISE STATED:X +/-0.10 ANGLES: +/-1°
 - 10.COMPLY WITH 920-000120-0000 (SUPPLIER PACKAGING STANDARDS).
 11. ALL MATERIALS AND PROCESSES USED IN THE MANUFACTURE OF THIS PRODUCT MUST MEET THE FOLLOWINGS:
 - 11.1. RoHS COMPLIANT-FOR TESTING METHODS AND LIMITS REFER TO ARTESYN P/N 970-006034-0000 (ITEM7).
 - 11.2. THE LATEST REQUIREMENT OF REACH REGULATION (<https://echa.europa.eu/candidate-list-table>)

P/N SUFFIX	REVISION	ECO NO.	REVISION DESCRIPTION	DATE	REVISED BY	APPROVED BY
-0000	AA		FIRST RELEASE	2020-08-28	JIAHAO CHEN	
SHEET REVISION HISTORY						

	Drawn to ISO STANDARDS Tolerancing ISO 8015	General tolerances Unless Stated Otherwise	Units LENGTH: mm ANGLE: DEG	Scale 1:1
	Material SEE NOTES	Treatment SEE NOTES	Document type DRAWING	Document status RELEASED
Created by JIAHAO CHEN	Approved by XIAOHUI WANG	Dept. CNZ	Technical reference JIAHAO CHEN	Title, Supplementary title LGA110D-01DADJJ PKG,T&R,CXR TP
	The Artesyn Embedded Technologies logo is a trademark of Artesyn Technologies, Inc.	Rev. AA	Date of issue 2020-08-28	Lang. Sheet EN 1 of 1